

# Performance Evaluation Zeta Converter Using PI Controller for Energy Management in DC Nanogrid Isolated System

Farid Dwi Murdianto<sup>1,a,\*</sup>, Indhana Sudiharto<sup>2,b</sup>, Ani Wulandari<sup>3,c</sup>

<sup>1</sup> Department of Electrical Engineering, Politeknik Elektronika Negeri Surabaya, Jl. Raya ITS, Keputih Sukolilo 60111, Indonesia

<sup>a,\*</sup>farid@pens.ac.id (Corresponding Author), <sup>b</sup> indhana@pens.ac.id, <sup>c</sup> eni124@gmail.com



**Abstract**— Renewable energy is needed as an alternative energy source. One of the implementations of renewable energy is the Solar Power Plant (SPP). SPP is a component that uses solar cells to convert solar energy into electrical energy. Unfortunately, the output power of this solar cell depends on the intensity of the light which causes the output power to enter the load to be unstable. Sometimes the PV power decrease because of the shading effect. From this problem a converter is needed to keep the system output voltage. The converter used in this research is the zeta converter. This Zeta converter can operate like a buck boost converter. The output of the system used is not stable. Therefore to stabilize it requires good control. In this paper using PI controller to control this system in order to keep the output system stable. 3. The error generated using the PI Control on the system is only 0.34%.

**Keywords**—Photovoltaic, Zeta Converter, PI Controller

## I. Introduction

In this modern era, technology is developing very rapidly. Many household appliances and other supporting equipment even more require electrical energy. While most of people still using electrical energy from PLN that's the fuels using fossil fuels. Therefore, the need for electrical energy consumption is increasing. From the increasing demand for electrical energy causes the depletion of fossil fuels. Because basically this fossil fuel is a fuel that cannot be renewed so that when this fuel runs out it can cause the electricity supply to run out. The role of using renewable energy can help overcome and contribute to energy generation. In addition, renewable energy has many advantages, one of which is not causing pollution. There are many types of renewable energy, however, what is currently being developed is solar cell. Solar cell is a component that converts sunlight into DC electrical energy. The power

is produced by PV depends on the intensity of light and the temperature that hits the surface of the PV. So that, when the amount of light intensity decreases, the power is produced by PV will also be decreased. Based on this problem, a system is required to stabilize the output power for example the used of Zeta converter, which is one of the converters that can transfer power from PV [1]-[3].

## II. Research Methodology

This proposed system requires a converter that can increase and decrease the output voltage. The Zeta converter is a non-isolated converter that has the same operating mode like the buck-boost converter [4]. The output voltage of this converter is maintained stable. Therefore, control is required to keep the output voltage stable.

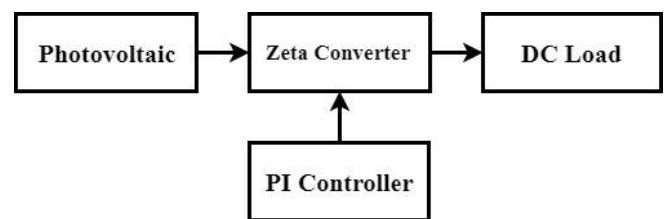


Figure 1. Overall System to Evaluate Performance PI Controller

The control used in this research is the PI controller. This Research is aimed to evaluate the performance PI Controller to manage the dynamic energy from PV.

**A. Modelling of Photovoltaic Module**

Solar cells are energy converters made from semiconductor devices. Some solar cells are assembled into solar panels that can convert solar energy into dc (direct current) electrical energy with a certain capacity according to the type of material and the area of the solar module [5]-[6]. Therefore, the using of more solar panels in areas that are not supplied by PLN (Indonesian Power Supplier Company) is suitable or could become a backup energy when the main electricity source is not available. The equivalent circuit of PV is shown in Figure 2.

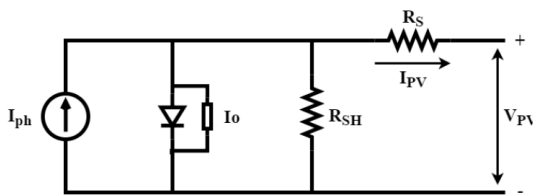


Figure 2. Equivalent circuit of Photovoltaic

The mathematical equation of the PV module can be expressed as [4]:

$$I_{pv} = N_p \cdot I_{ph} - N_p \cdot I_o \left[ \exp\left(\frac{q(V_{pv} + R_s \cdot I_{pv})}{A \cdot K \cdot T \cdot N_s}\right) - 1 \right] - N_p \cdot \left( \frac{V_{pv} + R_s \cdot I_{pv}}{N_s \cdot R_{SH}} \right) \quad (1)$$

where:

- I<sub>pv</sub> = output power pv module (A)
- V<sub>pv</sub> = output voltage PV (V)
- I<sub>PH</sub> = generated current (A)
- I<sub>O</sub> = current reverse diode (A)
- R<sub>s</sub> = resistance series solar cell (ohm)
- R<sub>SH</sub> = resistance shunt solar cell (ohm)
- N<sub>P</sub> = number of parallel solar cell
- N<sub>S</sub> = number of series solar cell
- Q = electronic charge (1.6 x 10<sup>-19</sup> C)
- K = constant boltzman (1.38 x 10<sup>-23</sup> J/K)

Table I shows data on solar panels with a maximum power of 100 Watt peak.

Table 1. Specification of Photovoltaic

Parameters	Specification
Maximum Power (P <sub>max</sub> )	100 W
Current at P <sub>max</sub> (I <sub>mp</sub> )	5.81 A
Voltage at P <sub>max</sub> (V <sub>mp</sub> )	17.2 V
Short circuit Current (I <sub>sc</sub> )	6.46 A

Open circuit Voltage(Voc)	21.6 V
Temperature coefficient of Voc	-0.38 %/°C
Temperature coefficient of I <sub>sc</sub>	0.065 %/°C

**B. Modelling of Zeta Converter**

The zeta converter topology provides a positive output voltage from the input voltage whose output voltage changes up and down. The Zeta converter is another option for regulating an intermittent input voltage. This Zeta converter is run under the Continuous Conduction Mode (CCM) condition [7].

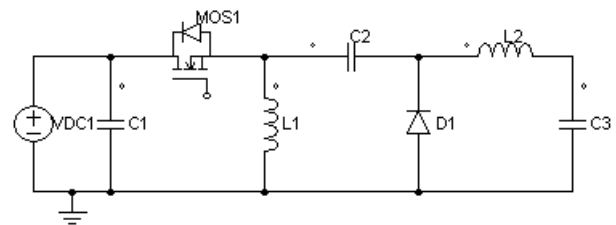


Figure 3. Zeta Converter Circuit

A Zeta converter typically has two inductors as shown in figure 3. The operation principle of the Zeta Converter can be elaborated as the following description.

When MOS1 is off, the voltage passing through L1b must be output voltage during parallel with the output capacitor. As long as the output capacitor is charged by the output voltage, the voltage will pass through MOS1 when MOS1 off is V<sub>IN</sub> + V<sub>OUT</sub> even though the voltage passing through L1a is -V<sub>OUT</sub> relative to the drain of MOS1. When MOS1 on, the coupling capacitor is charged by V<sub>OUT</sub>, which is connected in series with L1b, so the voltage passing through L1b is + V<sub>IN</sub>, and diode D1 is V<sub>IN</sub> + V<sub>OUT</sub>. From the basic operation, it can calculate the value of the duty cycle of the system. It is assumed that the efficiency value of the circuit is 100%. So the duty cycle value can be calculated using equation (2):

$$D = \frac{V_{out}}{V_{in} + V_{out}} \quad (2)$$

Then duty cycle value must be considered for the selection of components. The value of the selected component can be calculated using equation (3):

$$\Delta L(pp) = K \times I_{in} \quad (3)$$

$$C_{in(min)} = \frac{D \times I_{in}}{\Delta V_{cin} \times V_{in} \times f_{sw}} \tag{4}$$

$$C_c = \frac{D \times I_{in}}{\Delta V_{cc} \times V_{out} \times f_{sw}} \tag{5}$$

$$C_{out(min)} = \frac{D}{8 \times \Delta V_o \times f_{sw}} \tag{6}$$

Where:

- V<sub>in</sub> = input voltage (V)
- V<sub>out</sub>= output voltage (V)
- Δ<sub>IL(PP)</sub>= max ripple current output. (A)
- Δ<sub>vout</sub>= ripple output voltage (A)
- F<sub>sw</sub> = switching frequency (Hz)
- L1 = inductor 1 (H)
- L2 = inductor 2 (H)
- C<sub>c</sub> = coupling capacitor (F)
- C<sub>in</sub> = input capacitor (F)
- C<sub>out</sub>= output capacitor (F)

Table 2. Specification of Zeta Converter

Parameters	Symbol	Value	Units
Input voltage zeta mode	V <sub>in</sub>	60	Volt
Output voltage zeta mode	V <sub>out</sub>	206	Volt
Output Current ripple max	Δ <sub>IL(PP)</sub>	1.1	A
Voltage ripple max	Δ <sub>vout</sub>	0.206	Volt
Frekuensi switching	F <sub>sw</sub>	40	Khz
Inductor 1	L1	530	mH
Inductor 2	L2	530	mH
Coupling capacitor	C <sub>c</sub>	100	uF
Input capacitor	C <sub>in</sub>	13.78	uF
Output capacitor	C <sub>out</sub>	100	uF

C. Modelling of PI Controller

A proportional controller that provides a proportional control action with an error will result in an effect on the rise time and cause an offset error. An integral controller that provides control action proportional to the number of errors will have a good effect in reducing steady state errors but can result in deteriorating transient responses. Knowledge of the effects are caused by each controller that will be used in determining the values of proportional (K<sub>p</sub>) and integral reinforcement (K<sub>i</sub>). The basic table of knowledge of the relationship between reinforcement and its effect on PI controllers is shown in Table 3 [8]-[10].

Table 3. PI Controller Response

Variable	Rise Time	Overshoot	Setting Time	Offset
K <sub>p</sub>	Increasing	Decreasing	Small Change	Occurs
K <sub>i</sub>	Decreasing	Increases	Increases	Eliminate

Combined proportional control actions and integral control actions will perform the action of proportional and integral control (PI controller). This combination of actions has advantages compared to each of its constituents. The main advantage is that the advantages of each control action and the lack of control action can be overcome. In other words, the overall control elements of P and I aim to accelerate the reaction of a system and eliminate offsets [11].

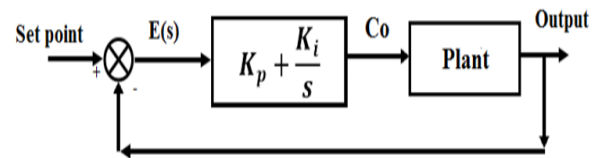


Figure 4. Block diagram of PI Controller

Figure 4 describes the diagram block and PI controller parameters. Where Co is the output signal from PI controller, K<sub>p</sub> is a proportional constant, T<sub>i</sub> is an integral time, K<sub>i</sub> is an integral constant, and E (s) is an error signal. Tuning on the PI controller is the determination of the magnitudes of P and I reinforcements so that good system characteristics are obtained. There are several ways of tuning the PI controller including the Ziegler-Nichols relay feedback method, manual (hand-tuning/trial-error), analytic method with optimization, pole placement, or auto-tune [12]-[13]. The flowchart of the system is shown in figure 5.

Figure 5 is the flowchart of the system. From the flowchart, it can be recognized that duty cycle defined by using PI controller. When duty cycle is lesser than desirable value, PI controller will increase the duty cycle automatically.

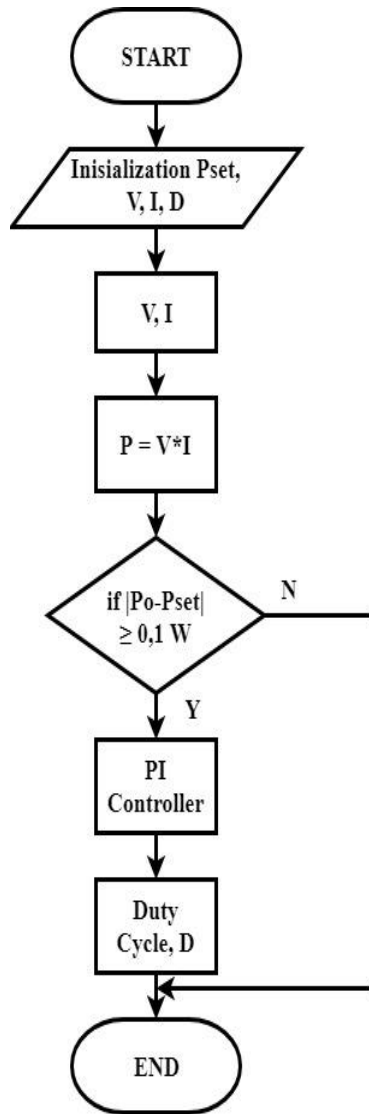


Figure 5. Block diagram of PI Controller

### III. Results and Discussion

The purpose of this simulation is to test the performance of the system produced by the PI controller. Figure 6 is a simulation result of zeta converter. In this simulation, PVs are used for the supply of the converter. Each PV has specification of 100 WP. The system using 12 PV which 4 PV are arranged in series and 3 PV are arranged in parallel. As a result, the output voltage PV is about 60 Volts.

From figure 6, it has been simulated using 12 PV with a power of 100 WP and a  $V_{mp}$  voltage of 17.2 Volt. This system is simulated using power simulator and after being simulated, it can be seen the form of response from the system when using the control or not.

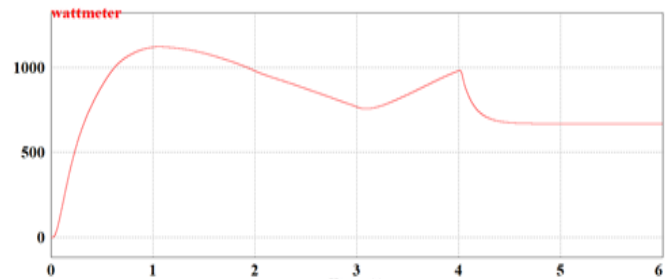


Figure 6. Response system without control when given disturbance

Figure 6 shown the response system without controller but the system is given disturbance at one of the photovoltaic. The setting point of duty cycle that is used same with design of this converter.

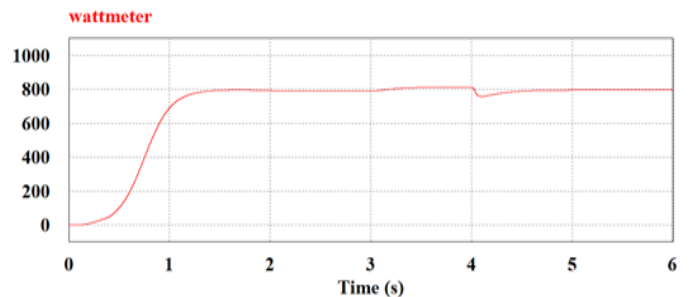


Figure 7. Response system with PI Controller when given disturbance

Figure 7 exhibits the respon system use control and given disturbance at 4 photovoltaics. The disturbance is given in 4s, the output power is back to set point. Setting point that given in the system is 800 Watt.



- [6] S. Siddiqua, S. Firuz, B. M. Nur, R. J. Shaon, S. J. Chowdhury and A. Azad, "Development of double burner smart electric stove powered by solar photovoltaic energy," 2016 IEEE Global Humanitarian Technology Conference (GHTC), 2016, pp. 451-458, doi: 10.1109/GHTC.2016.7857319.
- [7] Soedibyo, B. Amri and M. Ashari, "The comparative study of Buck-boost, Cuk, Sepic and Zeta converters for maximum power point tracking photovoltaic using P&O method," 2015 2nd International Conference on Information Technology, Computer, and Electrical Engineering (ICITACEE), 2015, pp. 327-332, doi: 10.1109/ICITACEE.2015.7437823.
- [8] R. Rakhmawati, F. Dwi Murdianto and M. Wildan Alim, "Soft Starting & Performance Evaluation of PI Speed Controller for Brushless DC Motor Using Three Phase Six Step Inverter," 2018 International Seminar on Application for Technology of Information and Communication, 2018, pp. 121-126, doi: 10.1109/ISEMANTIC.2018.8549749.
- [9] R. Rakhmawati, J. Hilmyvarafi Farrasbyan, Suhariningsih and F. Dwi Murdianto, "Performance Robustness of PID Controller in Buck Converter For Cooling System," 2018 International Seminar on Application for Technology of Information and Communication, 2018, pp. 127-132, doi: 10.1109/ISEMANTIC.2018.8549781.
- [10] M. Z. Efendi, F. Dwi Murdianto and H. N. Baweani, "Robustness Analysis of PID-Cuckoo Search Algorithm to Voltage Control in Three Phase of Synchronous Generator with Dynamic Load Condition," 2018 International Electronics Symposium on Engineering Technology and Applications (IES-ETA), 2018, pp. 133-138, doi: 10.1109/ELECSYM.2018.8615486.
- [11] I. Sudiharto, E. Sunarno, F. D. Murdianto and D. N. Kurniasari, "Robustness Analysis of PI Controller to Constant Output Power with Dynamic Load Condition in DC Nanogrid System," 2018 3rd International Conference on Information Technology, Information System and Electrical Engineering (ICITISEE), 2018, pp. 402-405, doi: 10.1109/ICITISEE.2018.8720976.
- [12] A. Jaya, E. Purwanto, M. B. Fauziah, F. D. Murdianto, G. Prabowo and M. R. Rusli, "Design of PID-fuzzy for speed control of brushless DC motor in dynamic electric vehicle to improve steady-state performance," 2017 International Electronics Symposium on Engineering Technology and Applications (IES-ETA), 2017, pp. 179-184, doi: 10.1109/ELECSYM.2017.8240399.
- [13] F. D. Murdianto, M. Z. Efendi, K. Widarsono and M. Azizi, "Robustness analysis of PID controller Bidirectional SEPIC/Zeta for Energy Management in DC Nanogrid Isolated System," 2018 3rd International Conference on Information Technology, Information System and Electrical Engineering (ICITISEE), 2018, pp. 406-411, doi: 10.1109/ICITISEE.2018.8721036.